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03-11-01

Docket No.: JCLA2561

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

CHIANG, Tsung-Pei

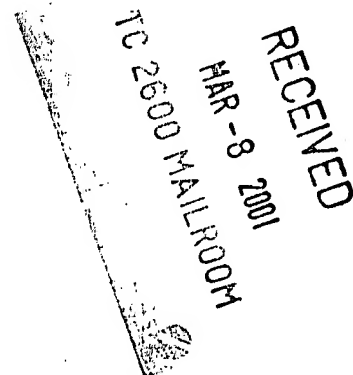
Serial No.: 09/235,265

Filed: Jan 22, 1999

For: Digital Liquid Crystal Display Driving Circuit

Examiner: CHOW, D

Art Unit: 2675



RESPONSE TO FINAL ACTION

Assistant Commissioner of Patents and Trademarks
Washington, DC 20231

Sir:

The Office Action mailed Nov. 27, 2000 (Paper No. 6), has been carefully considered. In response thereto, please enter the following amendments and consider the following remarks.

AMENDMENTS

Please amend the application as indicated hereafter.

In The Specification

On page 9, lines 12-19, please substitute the following clean copy text for the paragraph.

a! In operation, the output horizontal synchronizing signal Hs, the vertical Synchronizing signal Vs and the P-CLK signal from the timing control circuit 201 are directly transferred to the